## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

## In re Application of:

Kalvin Williams John Holcroft Christopher Lane

Serial No.:

10/015,076

Group Art Unit: 2112

Filed:

October 26, 2001

Examiner:

Lee, Christopher E.

For:

Hardware Semaphores For a Multi

Processor System Within A Shared

**Memory Architecture** 

**Atty Docket:** 

1496.00169 / 01-669

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Manu Kashyap

4/1/05

Date

SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85

## Official Draftsman

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation 1621 Barber Lane, MS D-106 Milipitas, CA 95035 408-433-7475

Date: 4/1/05

Respectfully submitted,

John Smitted

Henry Groth

Reg. No. 39,696